Electronic Acknowledgement Receipt FES ID: 1133212 Application Number: 10731714 Confirmation Number: 5466 At-speed ATPG testing and apparatus for SoC designs having multiple Title of Invention: clock domain using a VLCT test platform First Named Inventor: Anupama Aniruddha Agashe Customer Number: 23494 Filer: J. Dennis Moore/Shannon Trotter Filer Authorized By: J. Dennis Moore Attorney Docket Number: TI-36268 Receipt Date: 31-JUL-2006 Filing Date: 09-DEC-2003 Time Stamp: 10:35:51 Application Type: Utility International Application Number:

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages		
1		36268_Amnd_111.pdf	108596	yes	16		
	Multipart Description						
	Doc Desc		Start	End			
	Amendment - After Non-Final Rejection		1	1			
	Specification		2	8			
	Claims		9	14			
	Applicant Arguments/Remarks Made in an Amendment		15	16			
Warnings:							
Information:							
2	Fee Worksheet (PTO-875)	fee-info.pdf	8199	no	2		
Warnings:		ı			ı		
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.